

Docket No. 740756-1974

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT Application of:)
Yasuhiko TAKEMURA et al.) Art Unit 2822
Application Serial No. 09/321,715) Examiner: M. Wilczewski
Filed: May 28, 1999)
For: SEMICONDUCTOR DEVICE AND PROCESS)
FOR FABRICATING THE SAME) **PROPOSED**

AMENDMENT UNDER 37 C.F.R. 1.116

Box AF
Commissioner for Patents
Washington, D.C. 20231

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Sir:

Responsive to the final Office Action mailed November 27, 2001, please amend the above identified application as follows.

IN THE CLAIMS:

Please cancel claims 2, 10, 18, 26, 34 and 42.

Please amend the claims as follows.

1. (Four Times Amended) A method of manufacturing a semiconductor device having a plurality of thin film transistors, comprising the steps of:
forming a semiconductor film comprising silicon over a glass substrate;
crystallizing said semiconductor film; and
oxidizing the crystallized semiconductor film to be active layers of said thin film transistors in a pressurized atmosphere at a pressure greater than one atmosphere upto 15 atmospheres and at a temperature lower than a strain point of said glass substrate.

NVA216097.2

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From: Jerome W. Massie	Date: February 22, 2002	No. of Pages: (including this page) 30	Client/Matter: 740756-1974
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Comments:

RE APPLICATION SERIAL NO. 09/321,715

2822

Mr. Whitehead,

Thank you for receiving this proposed amendment to Ex. Wilczewski and forwarding the amendment and our request for an interview on Monday February 25, 2002 at any time convenient for the Examiner. Additionally, thank you for looking into the status related earlier filed application 09/222,185 which has not yet received a first office.

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